

SECTION 2: FORM PTO 1449 - MODIFIED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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09/932216  
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Applicant: Yang

Atty Dkt: 2550/111

Serial No: Not yet assigned

Date: August 17, 2001

Date Filed: Herewith

Invention: Bootstrap Module for Multi-Stage Circuit

Commissioner for Patents  
Washington, DC 20231

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS'  
INFORMATION DISCLOSURE STATEMENT

U.S. Documents

Exam. Ref.	Document	Issue			
<u>Init.</u>	<u>No.</u>	<u>Number</u>	<u>Date</u>	<u>Name</u>	<u>Class</u>
TDC	AA	6,060,937	May 9, 2000	Singer, et. al.	327/390
TDC	AB	6,118,326	Sept. 12, 2000	Singer, et. al.	327/390

Other Documents

Exam. Ref.		
<u>Init.</u>	<u>No.</u>	<u>Name</u>
TDC	AC	"A 1.5-V, 10-bit, 14.3-MS/s CMOS Pipeline Analog-to-Digital Converter", Abo, et. al., IEEE Journal of Solid-State Circuit, Vol. 34, No. 5, pp. 599-605, May 1999.

Examiner: T. Cunningham

Date Considered: 6/23/04

NOTE FOR EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance AND not considered. Include copy of this form with next communication to applicant.